Doc code: IDS
Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (03-09)
Approved for use through 04/30/2009. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

<table>
<tr><td rowspan="6">INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)</td></tr>
</table>

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10002217
	Filing Date	2001-11-01
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor
	Attorney Docket Number	CD01207M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code[1]	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Patent citation information please click the Add button.

U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code[1]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1					

If you wish to add additional U.S. Published Application citation information please click the Add button.

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number[3]	Country Code[2] i	Kind Code[4]	Publication Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear	T[5]
	1							☐

If you wish to add additional Foreign Patent Document citation information please click the Add button

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T[5]

		INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		
		Application Number	10002217	
		Filing Date	2001-11-01	
		First Named Inventor	Craig Nemecek	
		Art Unit	2123	
		Examiner Name	Jason Scott Proctor	
		Attorney Docket Number	CD01207M	

/JP/	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	☐
/JP/	2	USPTO Final Rejection for 09/975,115 (CD00182) dated 05/12/08; 33 pages	☐
/JP/	3	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 01/07/08; 30 pages	☐
/JP/	4	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 07/31/07; 28 pages	☐
/JP/	5	USPTO Final Rejection for 09/975,115 (CD00182) dated 02/21/07; 25 pages	☐
/JP/	6	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/31/06; 19 pages	☐
/JP/	7	USPTO Final Rejection for 09/975,115 (CD00182) dated 06/23/06; 20 pages	☐
/JP/	8	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 01/11/06; 15 pages	☐
/JP/	9	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 07/27/05; 11 pages	☐
/JP/	10	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 02/11/05; 86 pages	☐
/JP/	11	USPTO Final Rejection for 09/975,338 (CD00186) dated 01/31/08; 21 pages	☐

		Application Number	10002217
INFORMATION DISCLOSURE		Filing Date	2001-11-01
STATEMENT BY APPLICANT		First Named Inventor	Craig Nemecek
(Not for submission under 37 CFR 1.99)		Art Unit	2123
		Examiner Name	Jason Scott Proctor
		Attorney Docket Number	CD01207M

/JP/	12	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 08/14/07; 19 pages	☐
/JP/	13	USPTO Final Rejection for 09/975,338 (CD00186) dated 02/27/07; 23 pages	☐
/JP/	14	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 09/06/06; 11 pages	☐
/JP/	15	USPTO Advisory Action for 09/975,338 (CD00186) dated 05/15/06; 4 pages	☐
/JP/	16	USPTO Final Rejection for 09/975,338 (CD00186) dated 01/18/06; 12 pages	☐
/JP/	17	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 04/05/05; 13 pages	☐
/JP/	18	USPTO Final Rejection for 09/989,778 (CD01172) dated 03/16/09; 26 pages	☐
/JP/	19	USPTO Final Rejection for 09/989,765 (CD01194M) dated 03/31/09; 18 pages	☐
/JP/	20	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System;" 10/10/01; 09/975,105; Nemecek; 44 pages	☐
/JP/	21	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	☐
/JP/	22	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools"; Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	☐

/JP/	23	Microsoft Press Computer User's Dictionary; 1998; 3 pages **including page 18**	☐
/JP/	24	Sreeram Duvvuru and Siamak Arya, "Evaluation of a Branch Target Address Cache," 1995; IEEE, pages 173-180	☐
/JP/	25	Andrew S. Tanenbaum with contributions from James R. Goodman, "Structured Computer Organization," 1999, Prentice Hall, Fourth Edition	☐
/JP/	26	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events;" October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	☐
/JP/	27	USPTO Final Rejection for 09/994,601 (CD01170) dated 05/18/2007; 17 pages	☐
/JP/	28	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/2006; 17 pages	☐
/JP/	29	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	☐
/JP/	30	Julio Faura et al.; "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference; 4 pages	☐
/JP/	31	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	☐
/JP/	32	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	☐
/JP/	33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	☐

/JP/	34	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http>:// www.archive.org/web/20010219005250/http://cypressmicro.com- /t...>, Feb. 19, 2001; 21 pages	☐
/JP/	35	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Nework Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	☐
/JP/	36	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997; 4 pages	☐

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

| Examiner Signature | /Jason Proctor/ | Date Considered | 05/18/2009 |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. [2] Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). [3] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [4] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. [5] Applicant is to place a check mark here if English language translation is attached.